

10.1" IPS TFT LCD MODULE
MODEL: LXGD10.1-1280800-CWMN-03 Ver: 1.0

ROHS

< ◇> Preliminary Specification

< ◆> Finally Specification

CUSTOMER'S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

Revision History

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	10.1"	
LCD type	IPS TFT	
Display Mode	Transmissive/ Normally Black	
Resolution	1280 RGB x 800	Pixels
View Direction	FULL VIEWING	Best Image
Module Outline	229.46 (H) x149.1(V) x 2.5(T) (Note1)	mm
Active Area	216.96 (H) x 135.6(V)	mm
Pixel Size	169.5 (H) x 169.5(V)	um
Pixel Arrangement	RGB Stripe	
Surface treatment	Hard coating(3H)	
Display Colors	16.7M	
Interface	8 bits-LVDS interface	
With or Without Touch Panel	Without	
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	182	g

Note 1: Exclusive hooks, posts , FFC/FPC tail etc.

3. Absolute Maximum Ratings

V_{ss}=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.3	3.9	V
	AVDD	-0.3	14	V
	VGH	-0.3	42.0	V
	VGL	-19	0.3	V
Storage temperature	T _{STG}	-30	+80	°C
Operating temperature	T _{OP}	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage/current	VDD	2.3	2.5	3.6	V
	Ivdd	-	(130)	-	mA
	AVDD	8.0	8.2	8.4	V
	Iavdd	-	(45)	-	mA
	VGH	14.5	15	15.5	V
	Ivgh	-	(705)	-	uA
	VGL	-13.5	-13	-12.5	V
	Ivgl	-	(705)	-	uA
Input logic high voltage	VIH	0.8*VDD	-	VDD	V
Input logic low voltage	VIL	0	-	0.2*VDD	V

Note 1: Be sure to apply VDD and VGL to the LCD first, and then apply VGH.

Note 2: VDD setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 4: Typical VCOM is only a reference value, it must be optimized according to each LCM. Be sure to use VR.

5. Backlight Characteristic

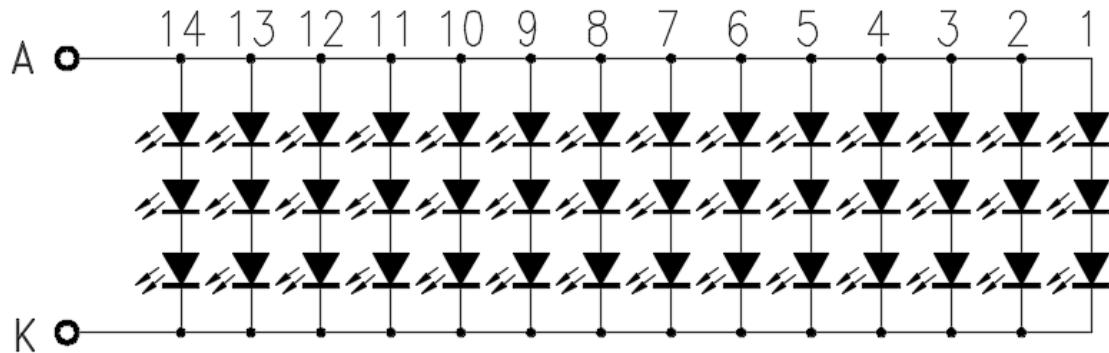
5.1. Backlight Characteristic

Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	V_F	$T_a=25\text{ }^{\circ}\text{C}$, $I_F=20\text{mA}/\text{LED}$	8.4	9.6	10.2	V
Forward Current	I_F	$T_a=25\text{ }^{\circ}\text{C}$, $V_F=3.2\text{V}/\text{LED}$	-	280	-	mA
Power dissipation	P_D		-	2688	-	mW
Uniformity	Avg		70	80	-	%
LED working life($25\text{ }^{\circ}\text{C}$)	-		20000	30000	-	Hrs
Drive method		Constant current				
LED Configuration		42 White LEDs(3 LEDs in one string and 14 groups in parallel)				

Note: LED life time defined as follows: The final brightness is at 50% of original brightness.

The environmental conducted under ambient air flow, at $T_a=25\pm 2\text{ }^{\circ}\text{C}$, $60\%\text{RH}\pm 5\%$, $I_F=20\text{mA}/\text{LED}$.

5.2. Backlighting circuit



6. Optical Characteristics

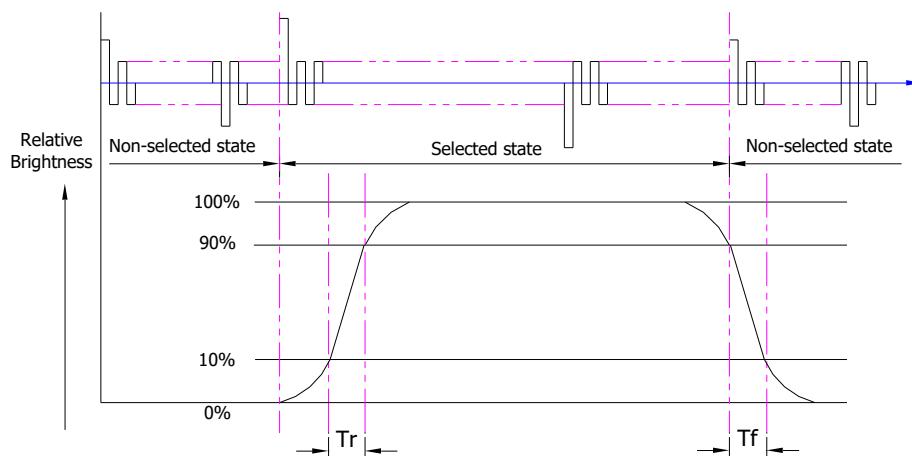
6.1. Optical Characteristics

$T_a=25^\circ\text{C}$, $V_{DD}=3.3\text{V}$

Transmissive (Transmissive On Backlight	Item	Symbol	Condition	Specification			Unit
				Min.	Typ.	Max.	
On	Luminance on TFT ($I_f = 20\text{mA/LED}$)	L_v	Normally viewing angle $\theta_x = \varphi_y = 0^\circ$	360	420	-	cd/m ²
	Contrast ratio(See 6.3)	CR		600	800	-	
	Response time (See 6.2)	T_R+T_F		-	25	50	ms
	Chromaticity Transmissive (See 6.5)	X _W					
		Y _W					
	Viewing Angle (See 6.4)	Horizontal	θ_x+	75	85	-	Deg.
			θ_x-	75	85	-	
		Vertical	φ_y+	75	85	-	
			φ_y-	75	85	-	
	NTSC Ratio(Gamut)			-	50	-	%

6.2. Definition of Response Time

6.2.1 Normally Black Type (Negative)

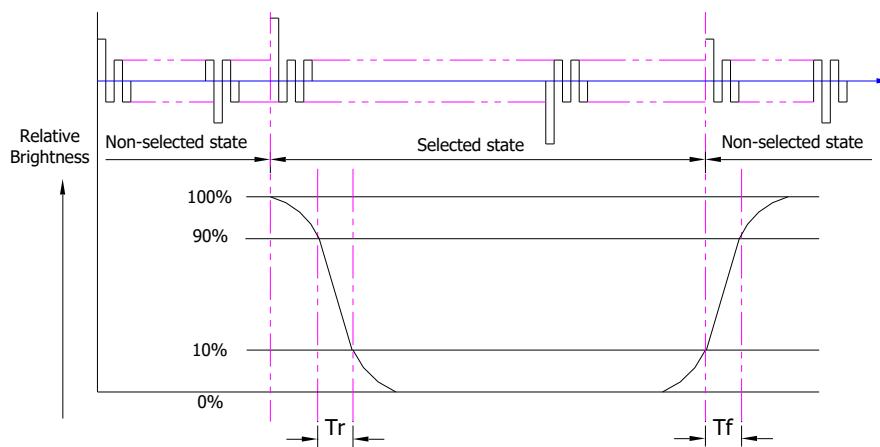


Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note : Measuring machine: LCD-5100

6.2.2 Normally White Type (Positive)



T_r is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

T_f is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note : Measuring machine: LCD-5100 or EQUI

6.3. Definition of Contrast Ratio

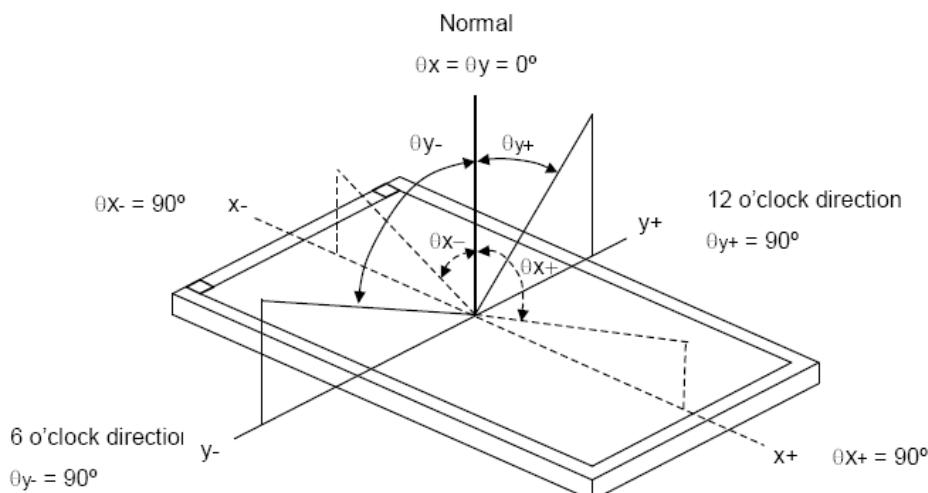
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles



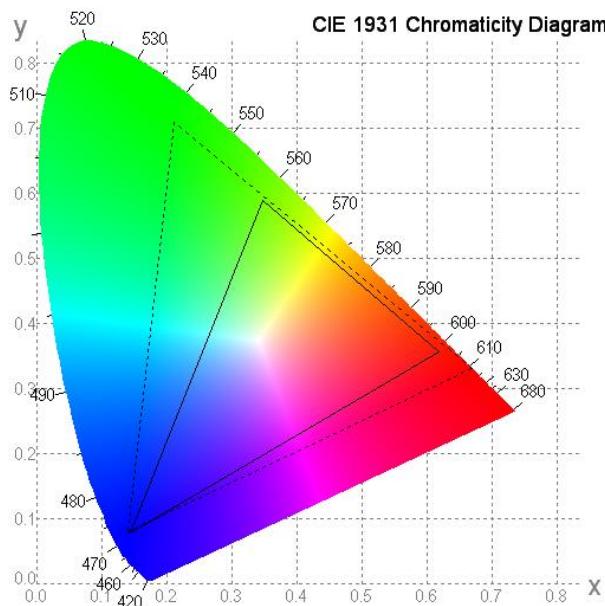
Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.6. Definition of Surface Luminance, Uniformity and Transmittance

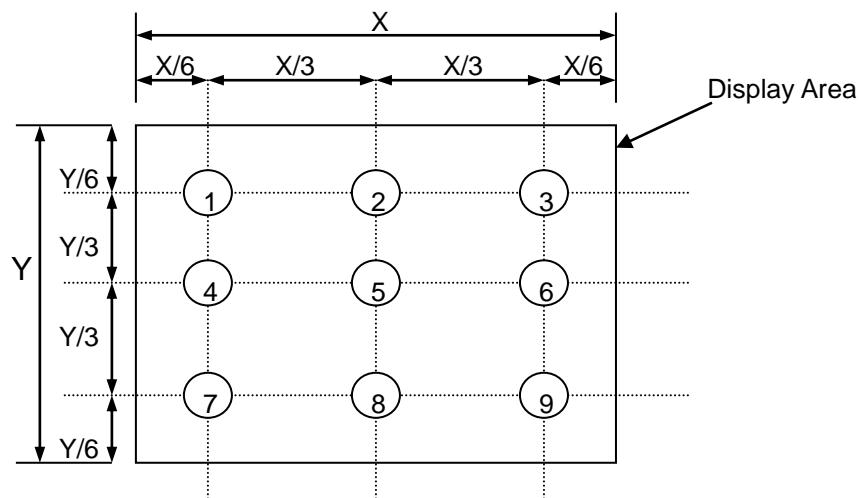
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.6.1 Surface Luminance: $L_v = \text{average } (L_{P1}:L_{P9})$

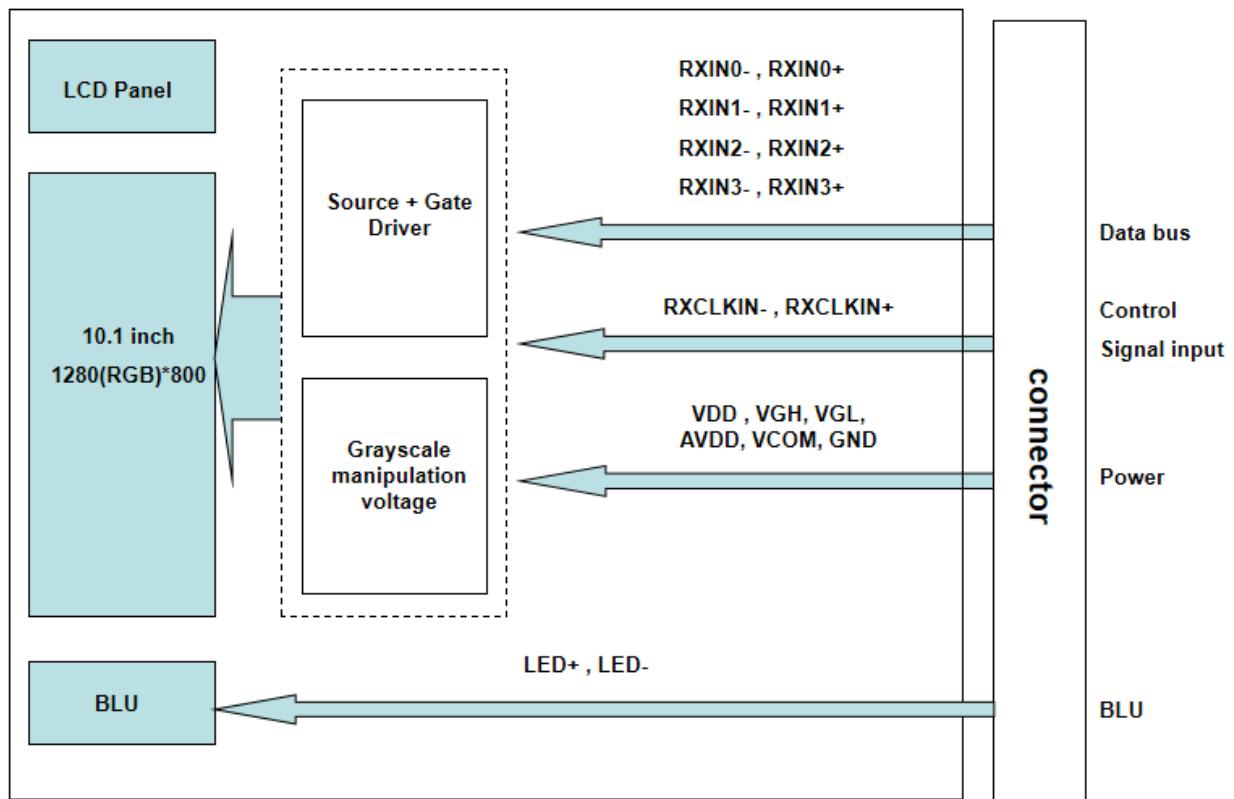
6.6.2 Uniformity = Minimal $(L_{P1}:L_{P9}) / \text{Maximal } (L_{P1}:L_{P9}) * 100\%$

6.6.3 Transmittance = $L_v \text{ on LCD} / L_v \text{ on Backlight} * 100\%$

Note : Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

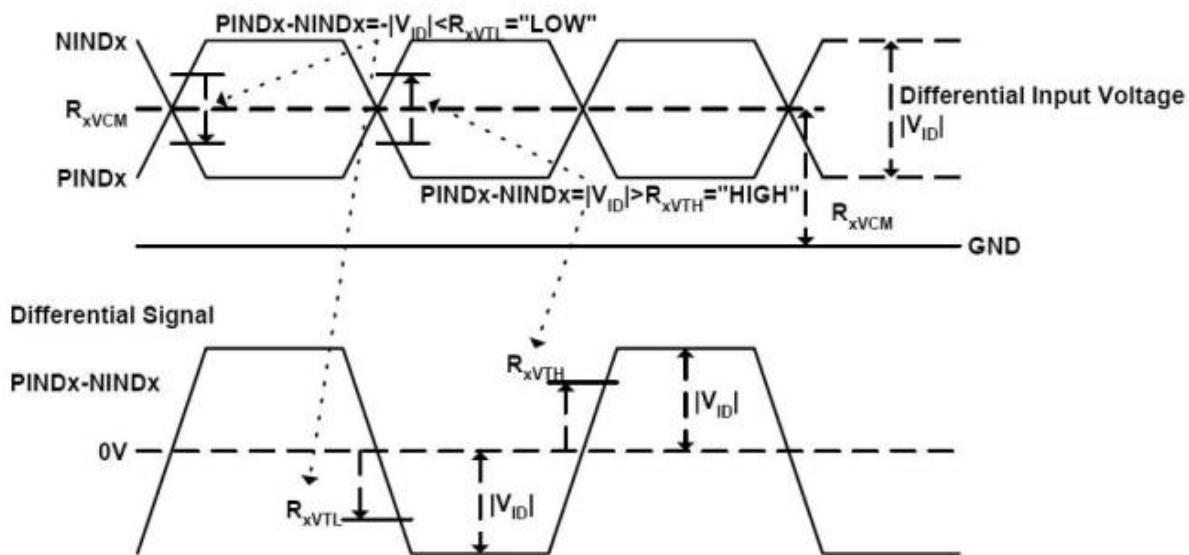
No.	Symbol	Function	Remark
1	NC	Common Voltage	
2	VDD	Power Voltage for digital circuit	
3	VDD	Power Voltage for digital circuit	
4	NC	No connection	
5	NC	No connection	
6	NC	No connection	
7	GND	Ground	
8	RXIN0-	-LVDS differential data input	
9	RXIN0+	+LVDS differential data input	
10	GND	Ground	
11	RXIN1-	-LVDS differential data input	
12	RXIN1+	+LVDS differential data input	
13	GND	Ground	
14	RXIN2-	-LVDS differential data input	
15	RXIN2+	+LVDS differential data input	
16	GND	Ground	
17	RXCLKIN-	-LVDS differential clock input	LVDS Clock
18	RXCLKIN+	+LVDS differential clock input	
19	GND	Ground	
20	RXIN3-	-LVDS differential data input	
21	RXIN3+	+LVDS differential data input	
22	GND	Ground	
23	NC	No connection	
24	NC	No connection	
25	GND	Ground	
26	NC	No connection	
27	NC	No connection	
28	NC	No connection	
29	AVDD	Power for Analog Circuit	
30	GND	Ground	
31	LED-	LED Cathode	
32	LED-	LED Cathode	
33	NC	No connection	
34	NC	No connection	
35	VGL	Gate OFF Voltage	
36	NC	No connection	
37	NC	No connection	
38	VGH	Gate ON Voltage	
39	LED+	LED Anode	
40	LED+	LED Anode	

9. Timing Characteristics

1) AC Electrical Characteristics

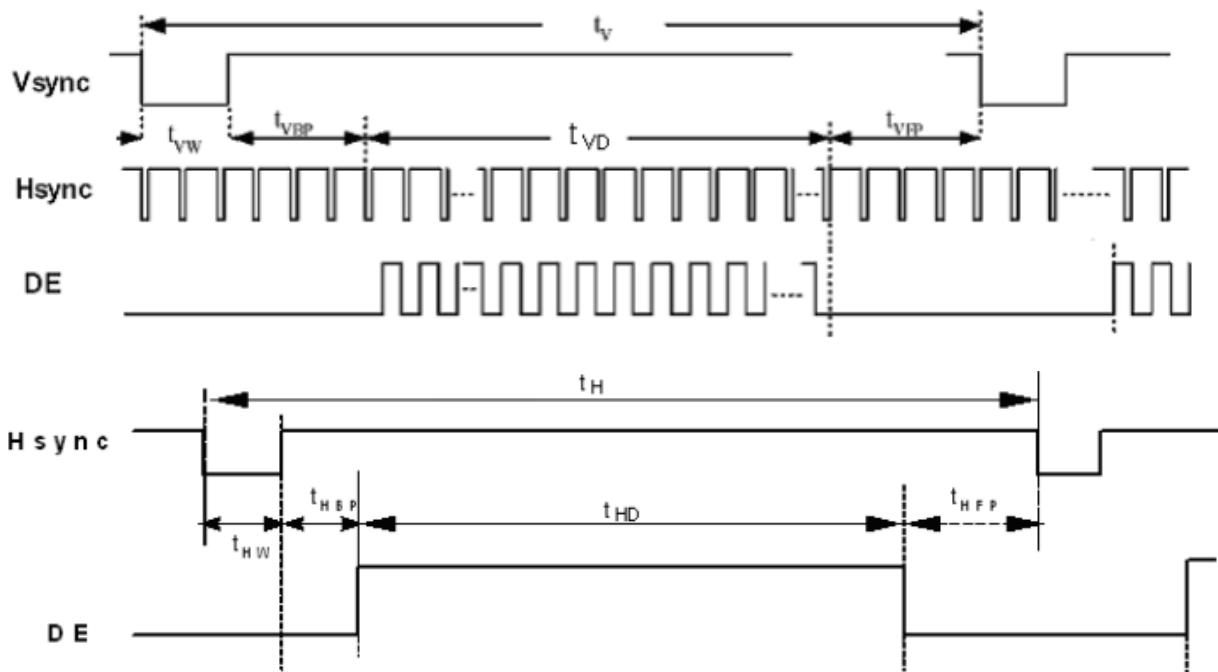
Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
LVDS Differential input high Threshold voltage	R_{xVTH}	-	-	+100	mV	
LVDS Differential input low Threshold voltage	R_{xVTL}	-100	-	-	mV	$R_{xVCM}=1.2V$
LVDS Differential input common mode voltage	R_{xVCM}	0.7	-	1.6	V	
LVDS Differential voltage	$ V_{ID} $	200	-	600	mV	

Single-end Signals

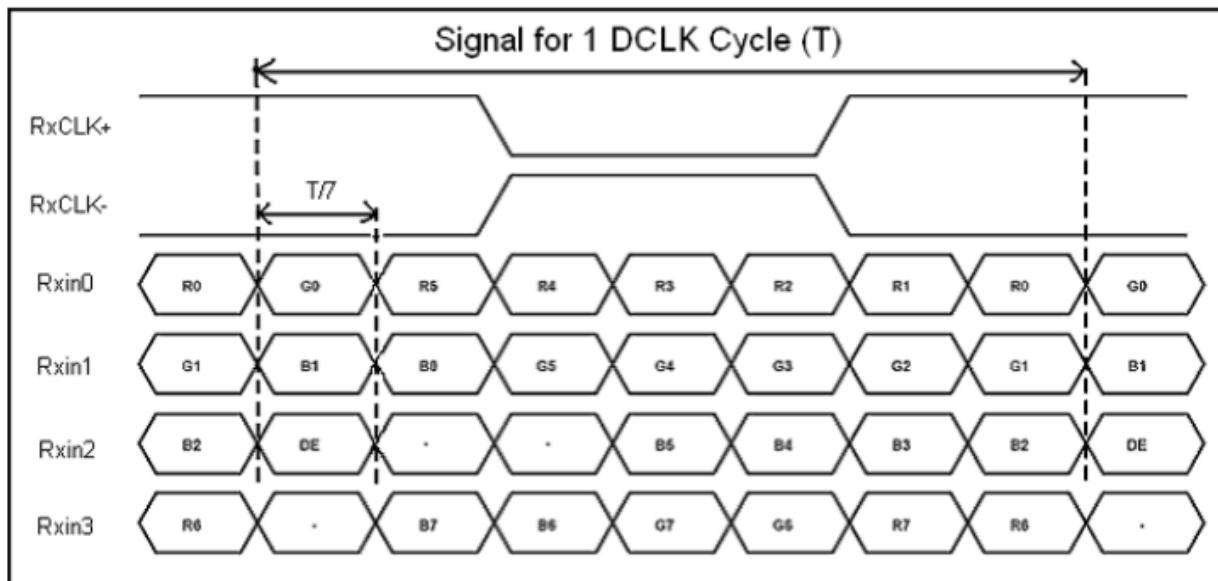


2) Timing Table

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock Frequency	$1/T_c$	68.9	71.1	73.4	MHz	Frame rate =60Hz
Horizontal display area	t_{HD}	1280				
HS period time	t_H	1410	1440	1470	Tc	
HS Width +Back Porch +Front Porch	$t_{HW}+t_{BPP}+t_{FP}$	60	160	190	Tc	
Vertical display area	t_{VD}	800				
VS period time	t_V	815	823	833	tH	
VS Width +Back Porch +Front Porch	$t_{VP}+t_{BP}+t_{FP}$	15	23	33	tH	

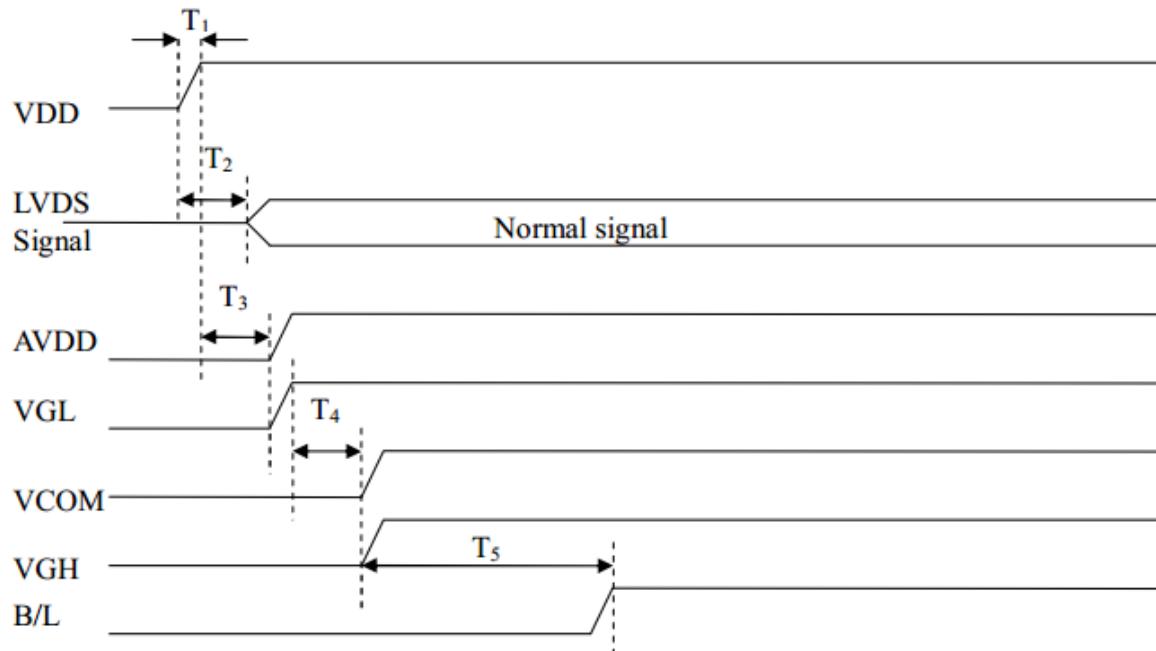


3) LVDS Data Input Format

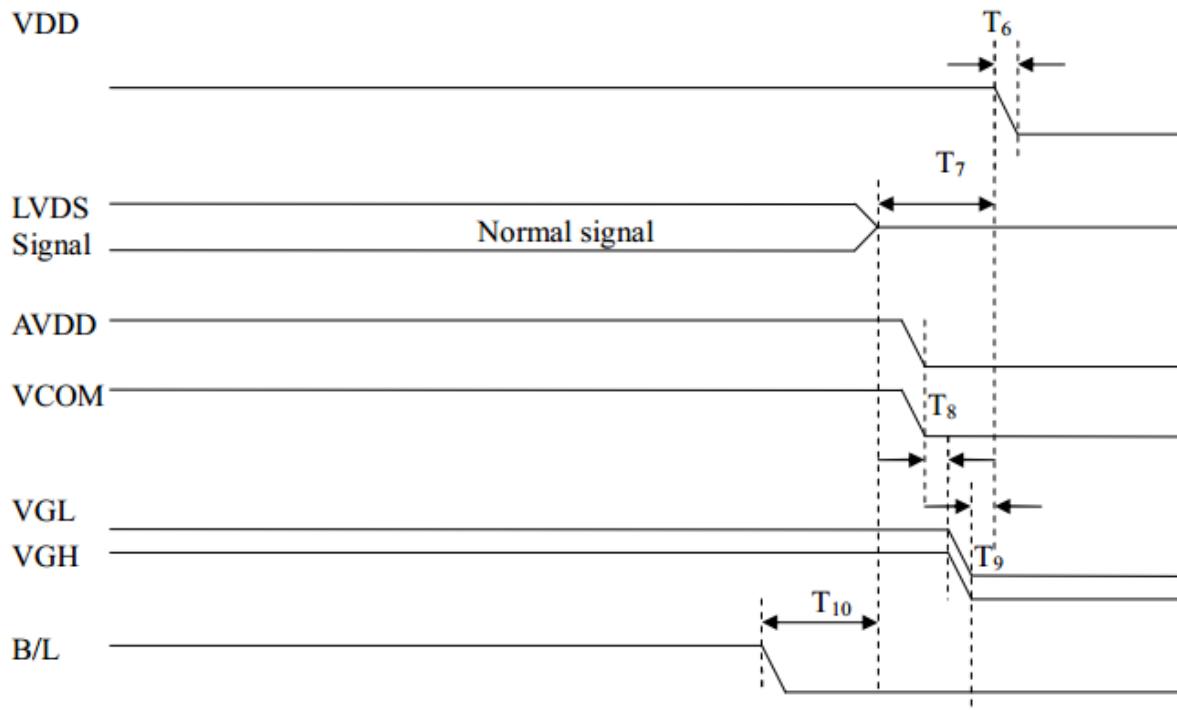


10. Power sequence

a. Power on:



Symbol	Value			Unit Min.
	Min.	Typ.	Max.	
T1	0.5	2	10	ms
T2	0	5	50	ms
T3	0	5	50	ms
T4	0	6	100	ms
T5	120	130	200	ms

b. Power off:

Symbol	Value			Unit Min.
	Min.	Typ.	Max.	
T6	0.5	2	10	ms
T7	0	7	50	ms
T8	0	5	10	ms
T9	0	1	10	ms
T10	0	2	100	ms

11. Quality Assurance

11.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

11.2.Standard for Quality Test

11.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

11.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

11.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

11.3.Nonconforming Analysis & Disposition

11.3.1. Nonconforming analysis:

11.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3. If can not finish the analysis on time, customer will be notified with the progress status.

11.3.2. Disposition of nonconforming:

11.3.2.1. Non-conforming product over PPM level will be replaced.

11.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

11.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1. There is any discrepancy in standard of quality assurance.

11.4.2. Additional requirement to be added in product specification.

11.4.3. Any other special problem.

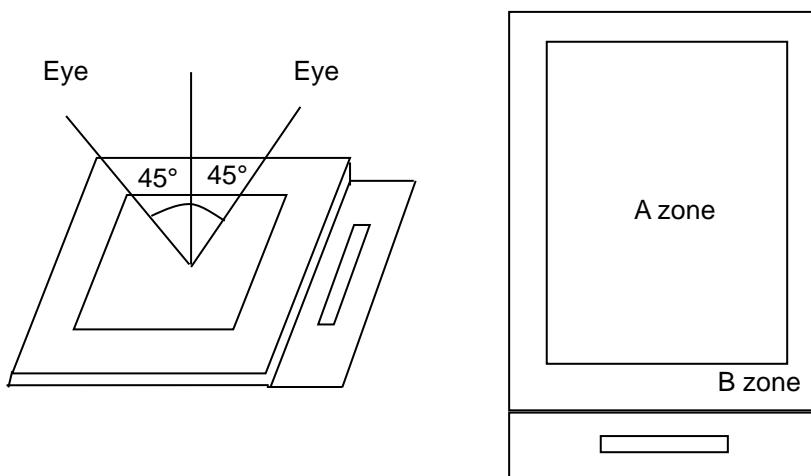
11.5.Standard of the Product Visual Inspection

11.5.1. Appearance inspection:

11.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,

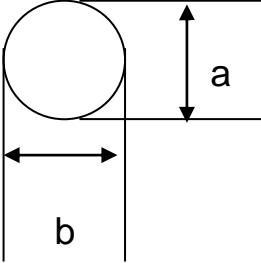


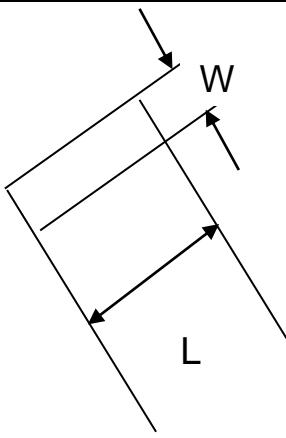
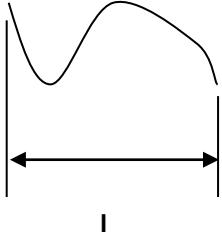
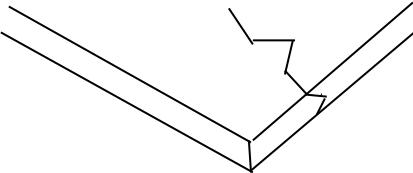
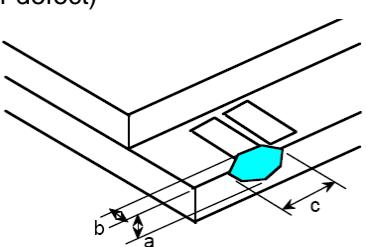
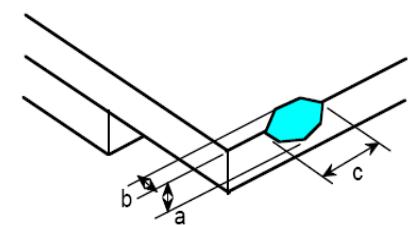
11.5.2. Basic principle:

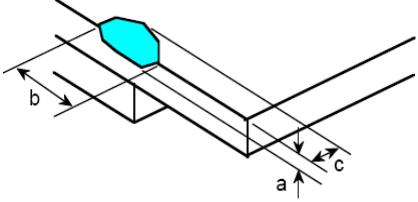
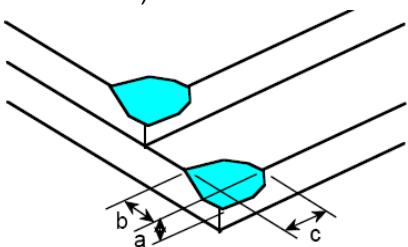
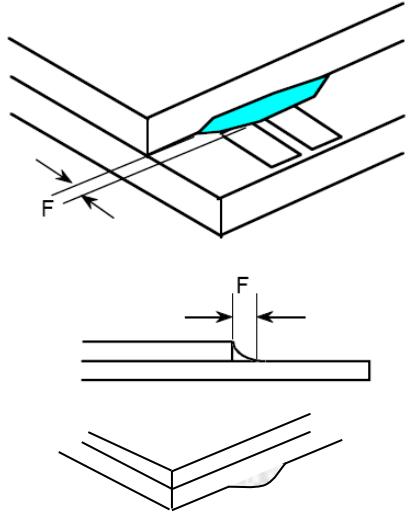
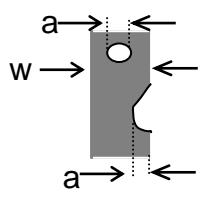
11.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

11.5.2.2. New item must be added on time when it is necessary.

11.6.Inspection Specification

No.	Item	Criteria (Unit: mm)																				
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 $\varphi = (a + b) / 2$ <p>Distance between 2 defects should more than 5mm apart.</p>	<table border="1"> <thead> <tr> <th rowspan="2">Size</th> <th colspan="2">Area</th> </tr> <tr> <th>Acc. Qty</th> <th></th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.20$</td> <td>Ignore</td> <td></td> </tr> <tr> <td>$0.20 < \varphi \leq 0.50$</td> <td>$N \leq 3$</td> <td></td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>0</td> <td></td> </tr> </tbody> </table>	Size	Area		Acc. Qty		$\varphi \leq 0.20$	Ignore		$0.20 < \varphi \leq 0.50$	$N \leq 3$		$0.50 < \varphi$	0						
Size	Area																					
	Acc. Qty																					
$\varphi \leq 0.20$	Ignore																					
$0.20 < \varphi \leq 0.50$	$N \leq 3$																					
$0.50 < \varphi$	0																					
02	Electrical Defect (Minor defect)	<table border="1"> <thead> <tr> <th rowspan="2">Bright dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="4">Note1</th> </tr> <tr> <th>$N \leq 2$</th> <th>$N \leq 2$</th> </tr> </thead> <tbody> <tr> <td>Dark dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> <td></td> </tr> <tr> <td>Total dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> <td></td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> <td>Note 2</td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>	Bright dot	Display Area	Total	Note1	$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 4$	$N \leq 4$		Total dot	$N \leq 4$	$N \leq 4$		Mura	Not visible through 5% ND filters.		Note 2		
Bright dot	Display Area	Total		Note1																		
	$N \leq 2$	$N \leq 2$																				
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Total dot	$N \leq 4$	$N \leq 4$																				
Mura	Not visible through 5% ND filters.		Note 2																			

03	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	  <table border="1" data-bbox="605 669 1224 922"> <thead> <tr> <th>Length</th><th>Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td>/</td><td>$W \leq 0.1$</td><td>Ignore</td></tr> <tr> <td>$L \leq 2.5$</td><td>$0.1 < W \leq 0.2$</td><td>3</td></tr> <tr> <td>$L > 2.5$</td><td>$0.2 < W$</td><td>0</td></tr> <tr> <td colspan="2">Total</td><td>3</td></tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
04	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>															
05	<p>Glass Chipping Pad Area: (Minor defect)</p>	 <table border="1" data-bbox="859 1461 1319 1641"> <thead> <tr> <th>Length and Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td><td>1</td></tr> <tr> <td>$c < 3.0, b < 1.0$</td><td>3</td></tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$								
Length and Width	Acc. Qty																
$c > 3.0, b < 1.0$	1																
$c < 3.0, b < 1.0$	3																
$a < \text{Glass Thickness}$																	
06	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p>	 <table border="1" data-bbox="859 1798 1319 2022"> <thead> <tr> <th>Length and Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td><td>1</td></tr> <tr> <td>$c < 3.0, b < 1.0$</td><td>2</td></tr> <tr> <td>$c < 3.0, b < 0.5$</td><td>4</td></tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$						
Length and Width	Acc. Qty																
$c > 3.0, b < 1.0$	1																
$c < 3.0, b < 1.0$	2																
$c < 3.0, b < 0.5$	4																
$a < \text{Glass Thickness}$																	

07	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
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08	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c < 3.0, b < 3.0$</td> <td>Ignore</td> </tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
09	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$F < 1.0$</td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											
10	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.)</p> <p>10.2 Open circuit is unacceptable.</p> <p>10.3 No oxidation, contamination and distortion.</p>										

11	Bubble on Polarizer (Minor defect)		Diameter	Acc. Qty
			$\varphi \leq 0.30$	Ignore
			$0.30 < \varphi \leq 0.50$	$N \leq 2$
			$0.50 < \varphi$	$N=0$
12	Dent on Polarizer (Minor defect)		Diameter	Acc. Qty
			$\varphi \leq 0.25$	Ignore
			$0.25 < \varphi \leq 0.50$	$N \leq 4$
			$0.50 < \varphi$	None
13	Bezel		13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.	
14	PCB		14.1 No distortion or contamination on PCB terminals. 14.2 All components on PCB must same as documented on the BOM/component layout. 14.3 Follow IPC-A-600F.	
15	Soldering		Follow IPC-A-610C standard	
16	Electrical Defect (Major defect)		The below defects must be rejected. 16.1 Missing vertical / horizontal segment, 16.2 Abnormal Display. 16.3 No function or no display. 16.4 Current exceeds product specifications. 16.5 LCD viewing angle defect. 16.6 No Backlight. 16.7 Dark Backlight. 16.8 Touch Panel no function.	

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

11.7. Classification of Defects

- 11.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.
- 11.7.2. Two minor defects are equal to one major in lot sampling inspection.

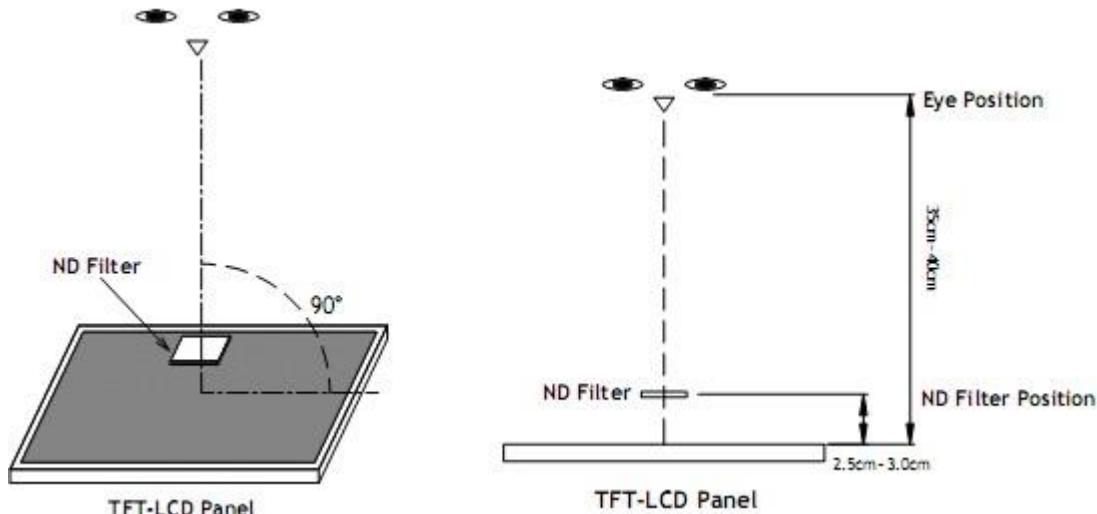
11.8. Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

11.9.Packing

- 11.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.
- 11.9.2. Modules inside package box should have compliant mark.
- 11.9.3. All direct package materials shall offer ESD protection.

Note1:Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixelarea.



Bright dot:The bright dot size defect at black display pattern.It can be recognized by 2% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Dark dot:Cyan,Magenta or Yellow dot size defect at white display pattern.It can be recognized by 5% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

12. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2 -2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1 -2008
3	High Humidity Storage	40°C, 90%RH, 96Hrs	2	GB/T2423.3 -2016
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2 -2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1 -2008
6	Thermal Cycling Test Storage	0°C, 60min~50°C, 60min, 20 cycles.	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.14 -2009
8	Electrical Static Discharge	Air: $\pm 4KV$ 150pF/330 Ω 5 times Contact: $\pm 2KV$ 150pF/330 Ω 5 times	2	GB/T17626.2 -2018
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

13. Precautions and Warranty

13.1.Safety

- 13.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 13.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

13.2.Handling

- 13.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 13.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

13.3.Storage

- 13.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 13.3.2. Strong light exposure causes degradation of polarizer and color filter

13.4.Metal Pin (Apply to Products with Metal Pins)

13.4.1. Pins of LCD and Backlight

- 13.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering
- 13.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

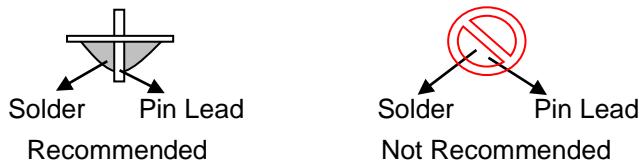
Maximum Solder Temperature: 370 °C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20 °C

Typical Soldering Time: ≤3s

13.4.1.3. Solder Wetting



13.4.2. Pins of EL

- 13.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.
- 13.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.
- 13.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290 °C

Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

13.4.2.4. No horizontal press on the EL leads during soldering.

13.4.2.5. 180° bend EL leads three times is not allowed.

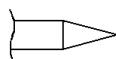
13.4.2.6. Solder Wetting



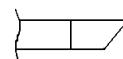
Recommended

Not Recommended

13.4.2.7. The type of the solder iron:

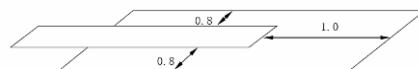


Recommended



Not Recommended

13.4.2.8. Solder Pad



13.5. Operation

- 13.5.1. Do not drive LCD with DC voltage
- 13.5.2. Response time will increase below lower temperature
- 13.5.3. Display may change color with different temperature
- 13.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear "fractured".
- 13.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 13.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 13.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 13.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

13.6. Static Electricity

- 13.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 13.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 13.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

13.7. Limited Warranty

- 13.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 13.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used
- 13.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

14. Packaging

TBD

15. Outline Drawing

